

N.E.

IN THE SPECIFICATION

Please replace paragraph [0026] with the following amended paragraph:

[0026] FIG. 1 shows an example of using different sampling patterns for x- and y-major lines. Each sample pattern has four sub-pixel points. Four is chosen as this gives good performance. In the case of the x-major line 102, using a vertical sampling pattern (black dots) shows 2 samples being covered by the line, giving 50% coverage of the pixel. This is a reasonable approximation. For the same line 102 a horizontal sampling pattern [[(grey dots)]] (striated dots) gives us no coverage which is incorrect. Similarly, for the y-major line 104, the vertical sampling pattern (black dots) incorrectly gives 100% coverage whereas the horizontal pattern [[(grey dots)]] (striated dots) gives reasonably accurate coverage of 75%.

Please replace paragraph [0166] with the following amended paragraph:

[0166] The primary cache is divided into two banks and each bank has 16 cache lines, each holding 16 texels in a 4x4 patch. The search is fully associative and 8 queries per cycle (4 in each bank) can be made. The replacement policy is LRU, but only on the set of cache lines not referenced by the current fragment or fragments in the latency FIFO. The banks are assigned so even mip map levels or 3D slices are in one bank while odd ones are in the other. The search key is based on the texel's index and texture ID not address in memory (saves having to compute 8 addresses). The cache coherency is only intended to work within a sub tile or maybe a tile and never between tiles. [[2]]

Please replace paragraph [0205] with the following amended paragraph:

[0205] Additional disclosure is found in nonprovisional application Ser. Nos. [[_____]] 10/071,895 filed Feb. 08, 2002 (TD-164) and [[_____]] 10/080,284 filed Feb. 20, 2002 (TD-169), both commonly owned, both copending with the present application, and both hereby incorporated by reference, and in provisional applications 60/267,265, 60/267,266, 60/269,462, 60/269,463, 60/269,428, 60/269,802, 60/269,935, 60/271,851, 60/271,795, 60/271,796, 60/272,125, and 60/272,516, various of which are referenced in the nonprovisional filings cited above, and all of which are hereby incorporated by reference.

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Please replace paragraph [0206] with the following amended paragraph:

[0206] According to a sample class of disclosed embodiments there is provided: A method for generating antialiased lines, comprising the actions of for each respective line, determining which of a plurality of orientation classes that line falls into; and performing subpixel sampling using one of a plurality of sampling patterns, in dependence on which of said plurality of orientation classes that line falls into; wherein said determination is made without the use of an error term or per pixel decisions.

Please replace paragraph [0207] with the following amended paragraph:

[0207] According to another sample class of disclosed embodiments there is provided:["]] A method for antialiased rendering, comprising the actions of: (a) identifying, for at least one respective line, which one of a limited number of directions is most nearly parallel to said line; and (b) performing subpixel sampling on said line with a subpixel sampling pattern which has maximal resolution approximately normal to said one direction; wherein said determination is made without the use of an error term or per pixel decisions.